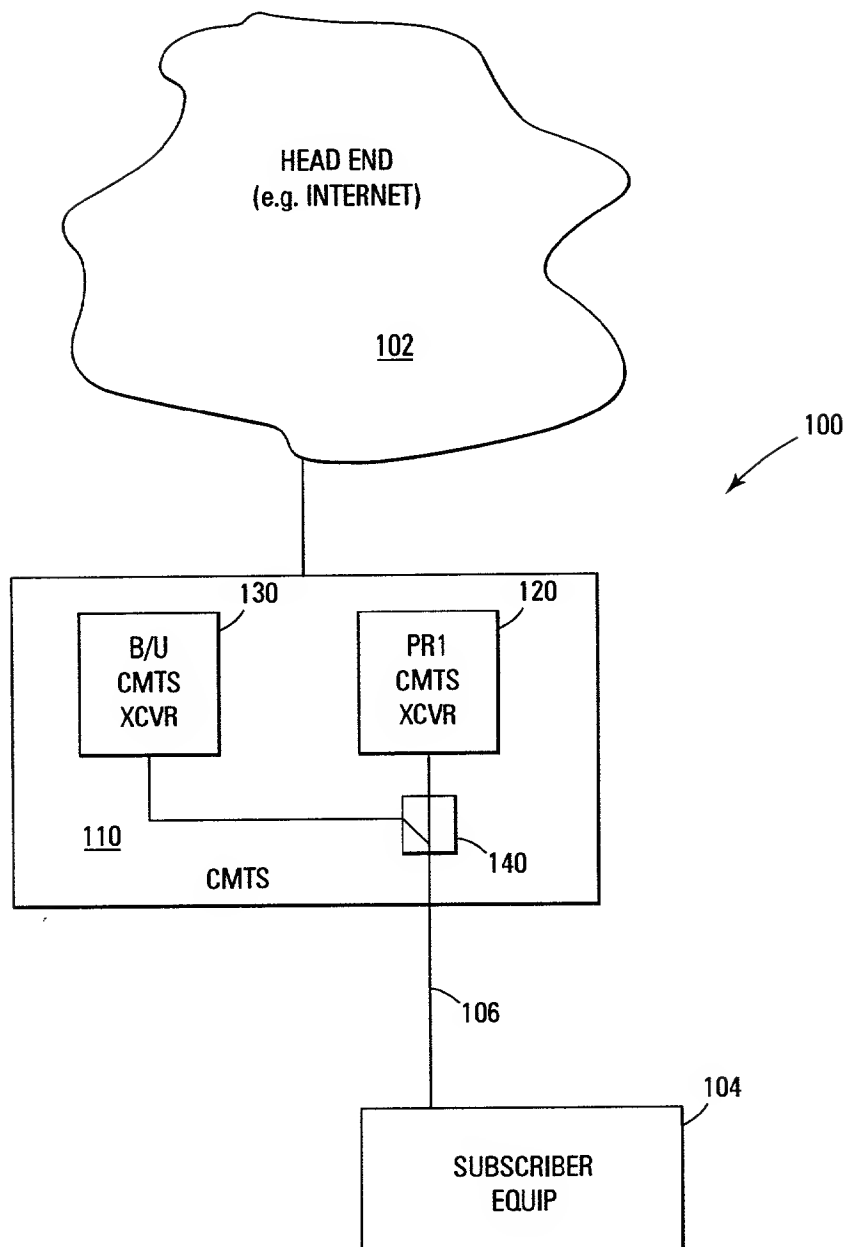
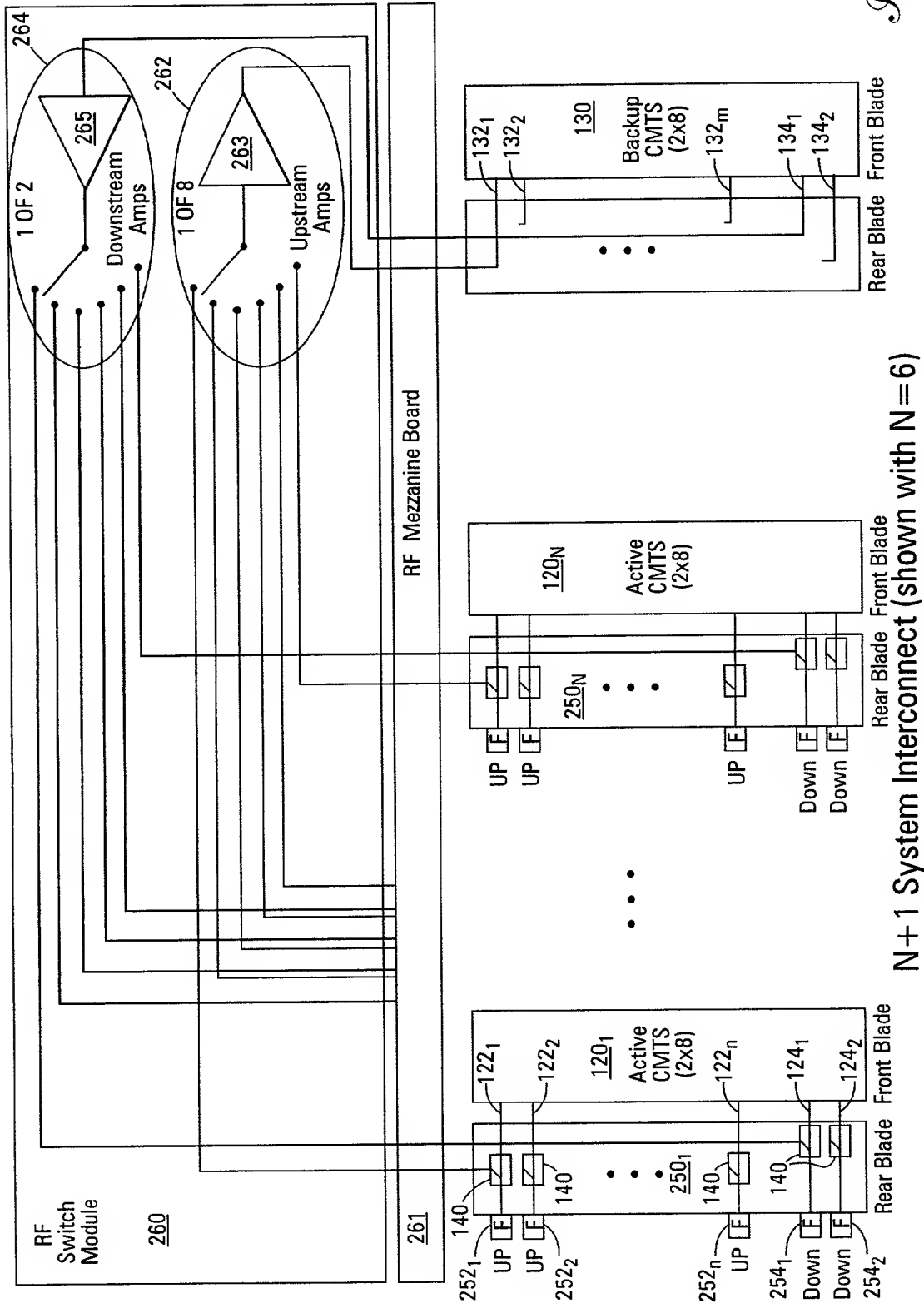


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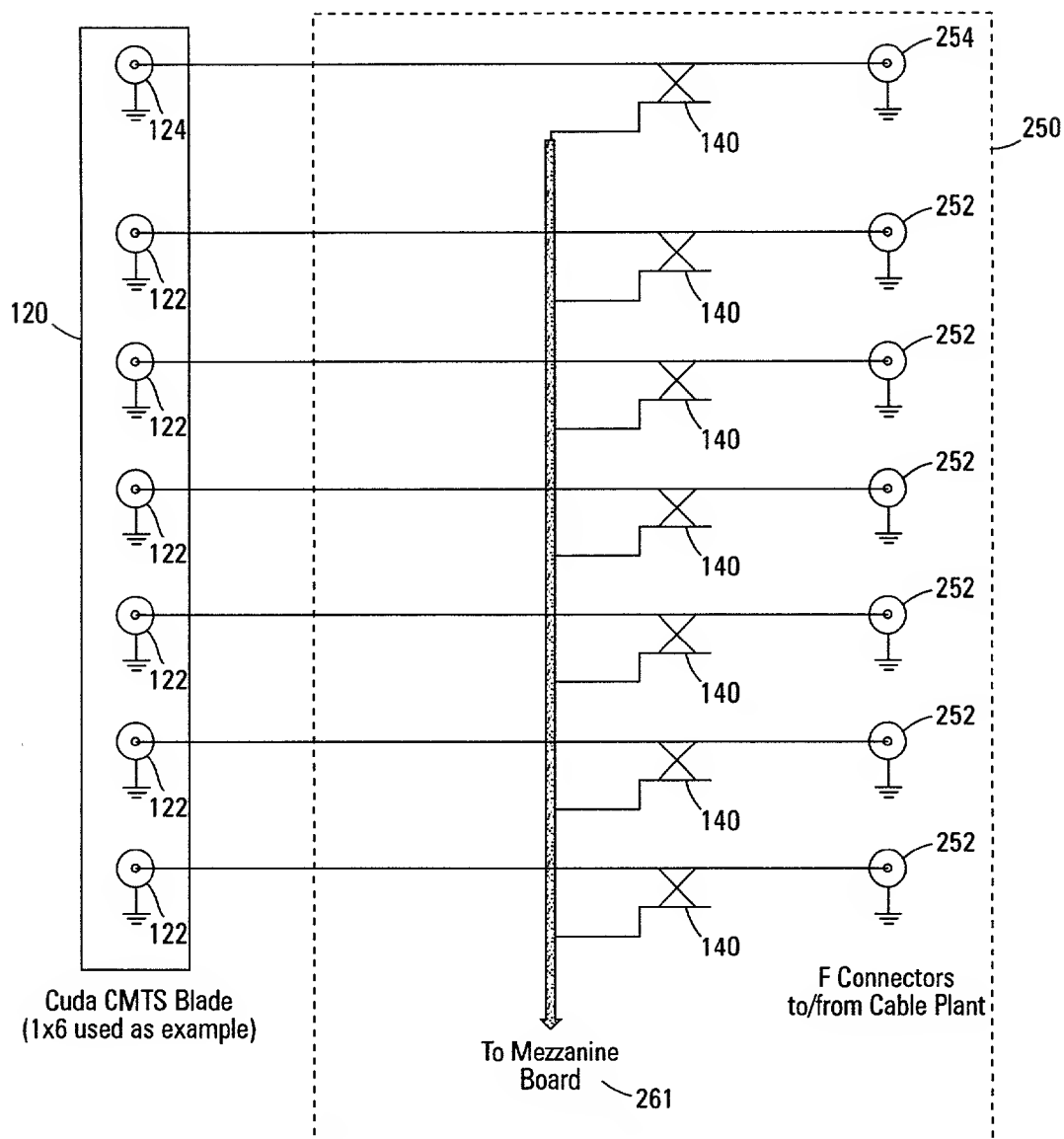
*Fig. 1*

Fig. 2



N+1 System Interconnect (shown with N=6)

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*Fig. 3*

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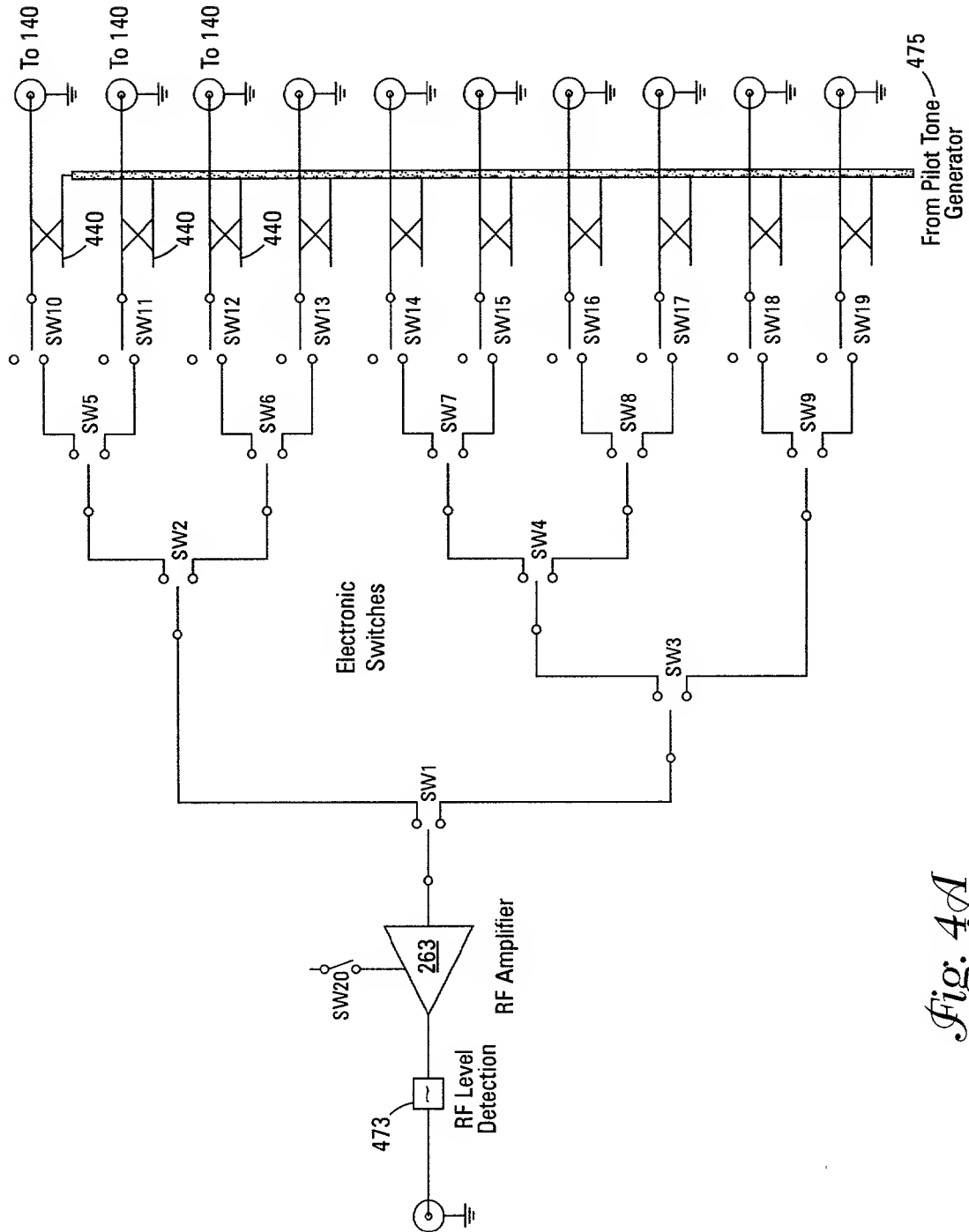


Fig. 4A

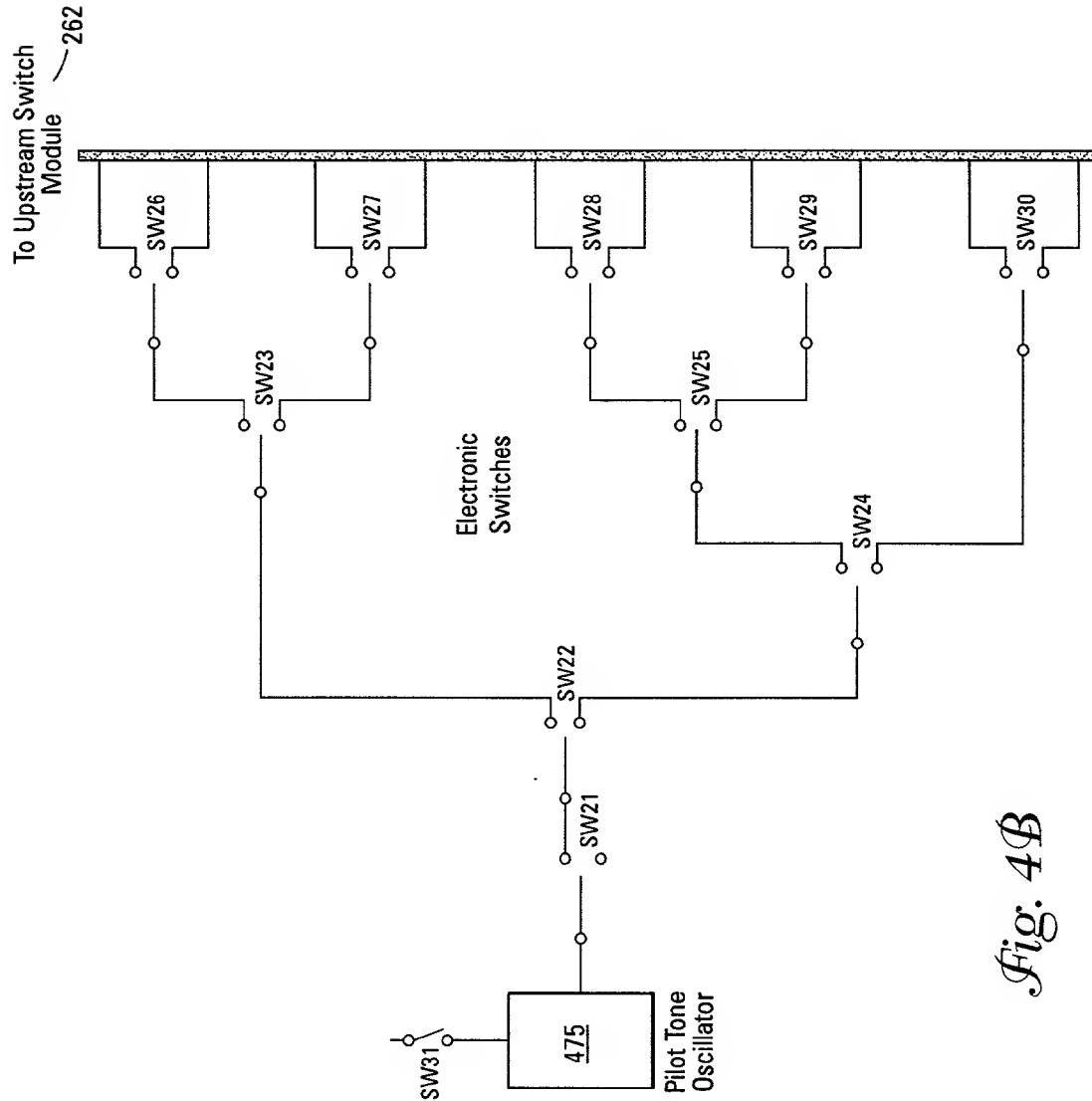


Fig. 4B

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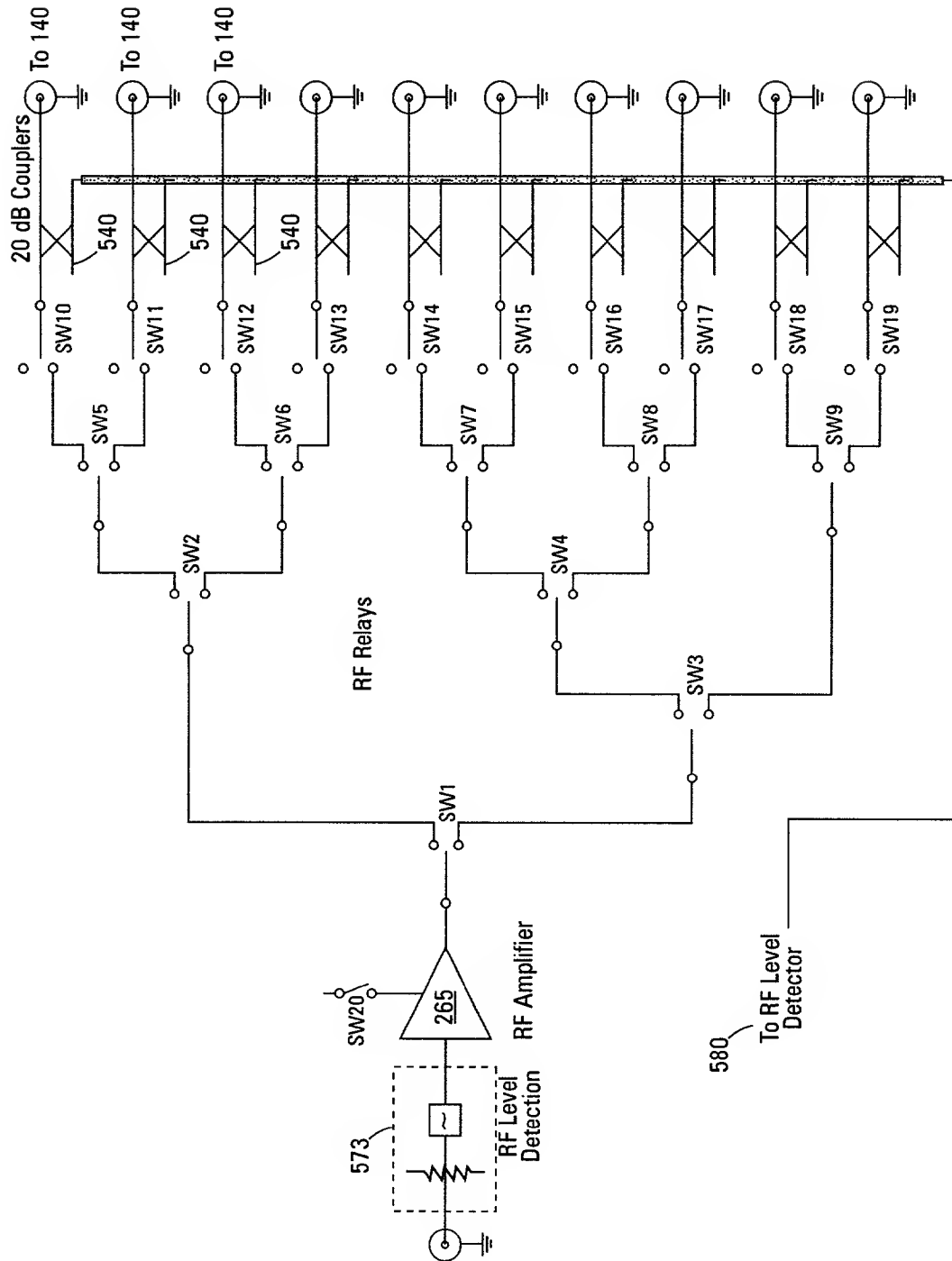


Fig. 5A

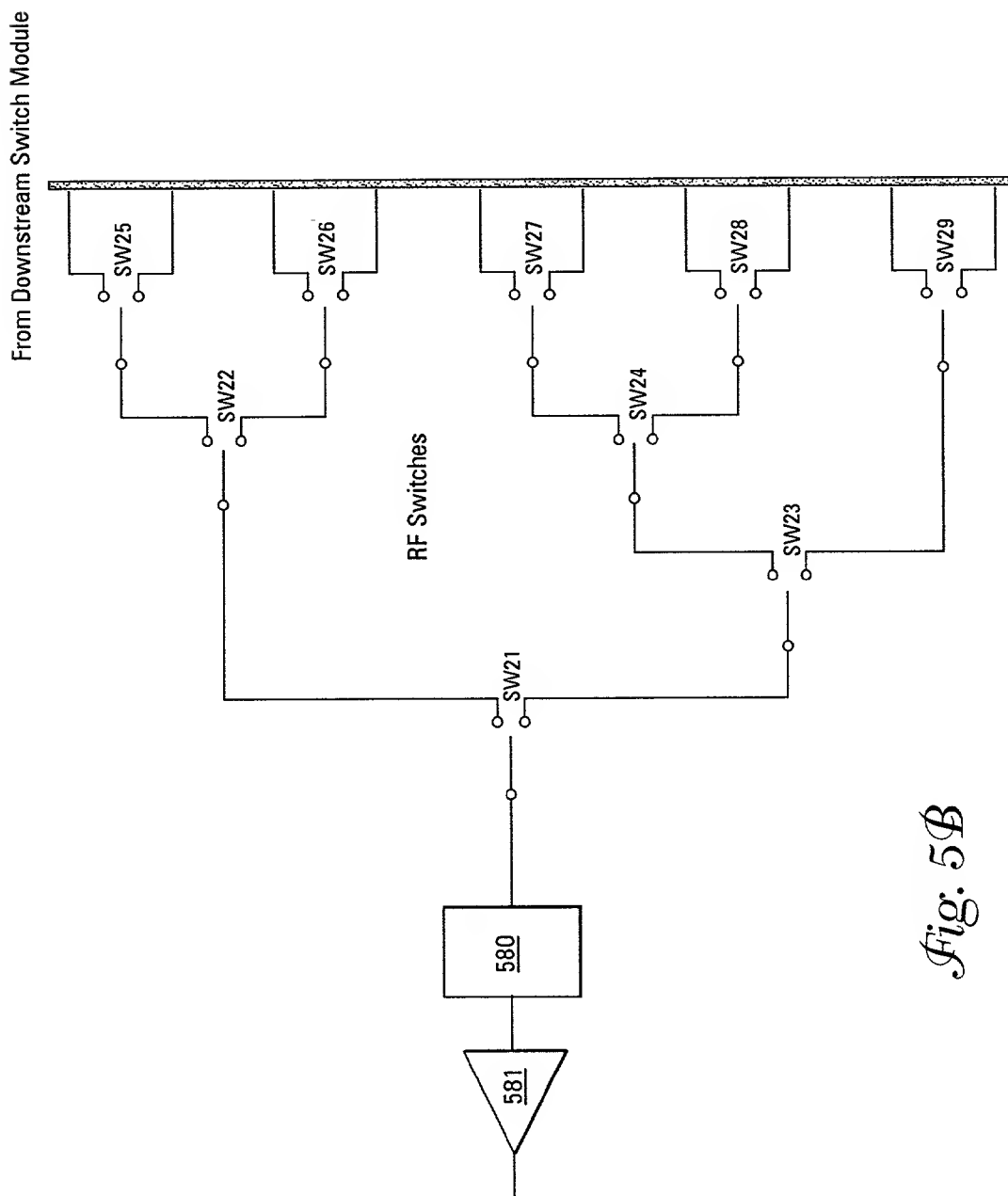
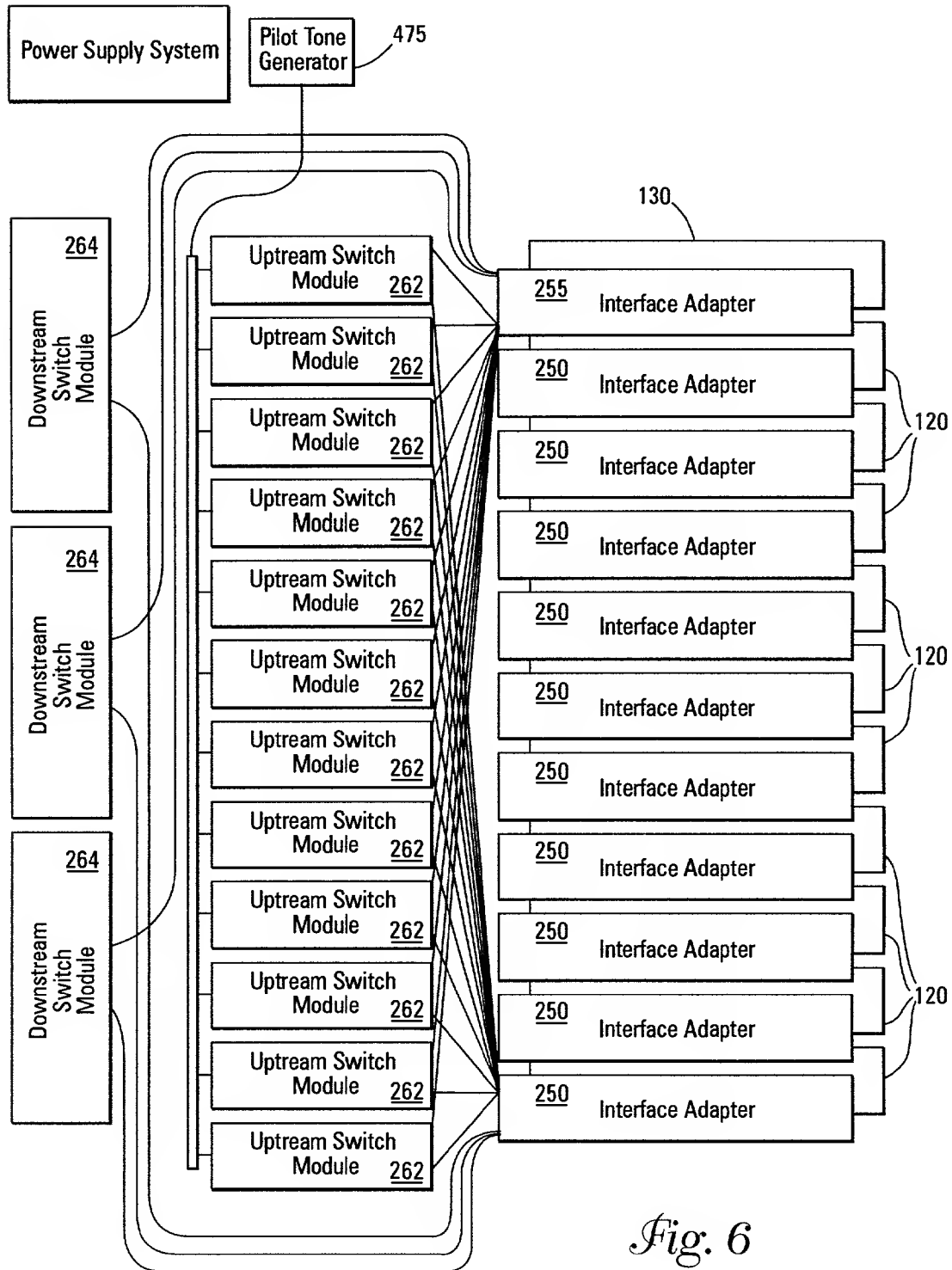


Fig. 5B

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*Fig. 6*